



04/26/00

Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

## U.S. UTILITY Patent Application

O.I.P.E. SCANNED <u>115</u> Q.A. <u>CTH</u>	PATENT DATE
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APPLICATION NO. 09/558377	CONT/PRIOR F	CLASS <del>709</del> 711	SUBCLASS <del>141</del>	ART UNIT <del>2750</del> 2788	EXAMINER <u>VERBRUGGE</u> <i>Redman</i>
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**TITLE** . **APPLICANTS**

Jean-Francois Autechaud

Modular interconnection architecture for an expandable multiprocessor machine, using a multilevel bus hierarchy and the same building block for all the levels

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
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